

EXPERIMENTAL STUDY ON BEHAVIOR OF MOV BASED SURGE ARRESTORS UNDER DISTORTED SUPPLY VOLTAGES

I. P. S. Ilangakoon

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Department of Electrical Engineering

University of Moratuwa

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Signature of the supervisor: Signature of the supervisor:
Dr. W. D. A. S. Rodrigo Senior Lecturer Grade I, Department of Electrical Engineering, University of Moratuwa, **if**

 $\frac{1}{2}$ 020 Date:

ABSTRACT

Surges due to lightning and various switching operations are a common phenomenon in electrical power systems. Due to surges, there can be transient overvoltages in the lines as high as many times the normal supply voltage. Equipment connected to the line cannot withstand these high voltages since the internal circuitry has not been designed to withstand surges. Metal oxide Varistor (MOV) is one of the best clamping devices used for electronic equipment from surges. Metal Oxide Surge Arrestor (MOSA) is made by stacking several numbers of MOV blocks to match system voltages for power system applications. Throughout life, MOVs are exposed to system voltage stress endlessly. Thus, the stress these devices experience is comparatively higher especially in the case of MOSAs.

Supply voltage distortion due to harmonics is another common phenomenon found in power systems due to modern non-linear loads connected to power systems. This study is conducted to access the effect of supply voltage harmonics on the life expectancy of MOVs. Electrical and thermal experiments performed to validate relevant models for MOVs. Using simulations, and a life expectancy model, the effect of various cases of voltage distortion studied to find the effect on life expectancy.

Based on the above results, recommendations have been made on how to select maximum continuous voltage of surge arrestors to minimize the effect of supply voltage harmonics on life expectancy.

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1. P. S. Ilangakoon March 2020

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1. INTRODUCTION

Metal Oxide Varistor (MOV) is the proven most popular building block of transient overvoltage protection devices used for decades in power systems and electrical/electronic appliances. Nonlinear V-I characteristics, fast response and high transient energy absorption capacity of MOVs are the prominent characteristics as a building block candidate for transient overvoltage protection devices. In literature, transient overvoltage protection devices are identified as Surge Protection Devices (SPDs), Lightning Arrestors and Surge Arrestors. MOV is the basic building block for many of these devices. Some types of devices contain transient overvoltage protection devices like Gas Discharge Tubes (GDT) or Transient Voltage Suppression (TVS) Diodes together with MOVs. The surge arrestors made of MOVs are identified as Meta Oxide Surge Arrestors (MOSA)

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Throughout the service life, the Surge Protection Devices (SPDs) made of MOVs are exposed to the system voltage. As an example, in a 400v Ac system, MOV connected between neutral and a phase will be exposed to a 230Vrms voltage waveform throughout the service life. SPD will conduct very small leakage current (in the order of ³⁰ - ³⁰⁰ micro Amperes) at the rated voltage of the system, If transient overvoltages are present, MOV starts to conduct and maintain the voltage at a guaranteed level up to its capability limits.

Supply voltage distortion is an inevitable factor in power systems [10, 11]. Distortion occurs due to the association of nonlinear loads to the system. Nonlinear loads have become a reason for nonlinear voltage drops across the impedance of power system components and hence system voltage gets distorted in the power system. IEEE-519- 2014 standard [2] has laid maximum values for the possible distortion at the Point of Common Coupling (PCC). Values are given the table below (Tablel-1)

Table 1-1: IEEE-519-2014 Voltage Distortion Limits

Table 1—Voltage distortion limits

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^aHigh-voltage systems can have up to 2.0% THD where the cause is an HVDC terminal whose effects will have attenuated at points in the network where future users may be connected.

Source: IEEE 519-2014 - IEEE Recommended Practice and Requirements for Harmonic Control in Electric Power Systems.

Stable operation of MOVs is a critical factor for the safe operation of protected downstream systems. The stability referrers to as the operation of MOV within the expected $V - I$ characteristic limits. Environmental conditions, as well as the characteristics of the supply voltage they are exposed to [18], are the factors for loss of stability. The loss of stability is identified as the failure is referred to as the MOV failure [15, 16]. MOVs fail catastrophically followed by the loss of stability due to the excess heat generated inside the MOV. In the literature electrical degradation of MOVs has been identified due to two factors namely exposure to AC or DC voltages for a prolonged period $[3,12]$ and due to surge currents discharged through the MOV $[13, 12]$ 14]. Quantification of degradation is done by measuring the increment in the clamping voltage.

Under this research work, the relationship of the supply voltage distortion with the behavior of MOVs is studied under rated voltage conditions. Different levels of voltage distortions are generated and applied to a set of samples of MOVs and the change in the leakage current is investigated. Higher leakage current is a sign of failure in the MOV. There will be a high level of power dissipation as well and hence a

temperature rise. This extra stress will degrade the MOV and it becomes a cause for eventual thermal run off of the MOV.

1. Introduction to MOVs

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Metal Oxide Varistors are nonlinear resistors which exhibit electrical propertied equivalent to back to back Zener diodes. The main materials MOVs are made of are metal oxides such as Zinc, Bismuth, and Manganese. A ceramic semiconductor is made by sintering the metal oxide during the manufacturing process, microstructure made during the process is crystalline and hence the MOV is inherited the capability of dissipating huge amounts of energy in transients. This is the reason why MOVs are the application for suppression of high energy transients occurring in electrical power systems and electronic circuits. The shape of the MOVs can be different due to the flexibility of the manufacturing process. The disk shape with leads radial is the shape found commonly. The

1.1.1 Physical Characteristics

The transients in the power systems can be categorized as external transients and internal transients. External transients are essentially lightning while internal transients are occurring due to the following reasons.

- 1. Switching relays
- 2. Switching inductive loads
- 3. Discharging capacitors

MOVs are used to protect electronic circuits form both of the above types of transients and MOV is used as a building block for making MOSAs that are used in power system applications. Typical current ratings are from 20A to 500A while the energy ratings are between 0.05J to 2.5J[1]

The varistor core is made of ZnO semiconductor grains. Their average size "d" is shown in the model in figure 1-1 below. The resistivity of ZnO is less than 0.3 Ω -cm [1].

Figure 1-1: Schematic representation of the mierostructure of a MOV, grains of conducting ZnO (average-sized) are separated *by* intergranular **boundaries [1]**

A cross-section of the material is shown in Figure 1-2, which illustrates the ceramic microstructure [1]. Each ZnO grain of the ceramic acts like a semiconductor junction at the grain boundary [1].

Figure 1-2: Optical photomicrograph of a polished and etched section of a varistor [1]

Varistors are fabricated by forming and sintering Zinc Oxide-based powders into ceramic parts [21]. These parts are then electroded with either thick film Silver or arc/flame sprayed metal [21]. Since the nonlinear electrical behavior occurs at the boundary of each semiconducting ZnO grain, the varistor can be considered a "multijunction" device composed of many series and parallel connections of grain boundaries [1].

1.1.2 Theory of Operation

The internal core structure of MOV is polycrystalline, hence their characteristics are different and complex in comparison to traditional semiconductors. Using sensitive measurement equipment, the characterization of the Voltage and current behavior of MOVs has been performed to a certain extent. Manufacturers are still working to better understand the characteristics beyond today's knowledge. Understanding the electronic phenomena taking place between grain borders, or junctions between the ZnO grains is the basis for understanding the characteristics of MOVs.

One of the theories proposed that electronic tunneling happened via an insulating second phase layer at the grain boundaries. A series and parallel assembly of semiconductor diodes is a better model proposed which matches best with varistor characteristics. Grain boundaries in this model consists of deflect states trapping free electrons from n-type ZnO grains. They form a layer of charge depletion in the nearby area to the grain boundaries.[l]

1000 Angstrom is the measured average value of the depletion layer calculated. Other model are proposing single-junction operations. Depletion layers in the single-junction model block the flow of electrons, because of this, the high impedance characteristics at low voltage are attributed. Under low voltage conditions, a leakage current flows which is due to the free flow of electrons through the grain boundaries. This current is sensitive to the temperature.[1]

1.1.3 Electrical Properties

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Typical V-l Characteristics of a MOV is sown in the figure below figure 1-3. These nonlinear characteristics can be divided into 3 regions [1].

Figure 1-3: MOV V-I Characteristics [1]

Region A: Normal working zone: Current between 10 μ A to 300 μ A [1].

Region B: Protection (clamping) Region: Voltage is kept nearly constant protecting to downstream [1].

Region C: Maximum surge current: The maximum peak current that the varistor can withstand (only) once in its lifetime [1].

1.1.4 Definitions

Maximum Continuous Voltage:

The maximum value of the voltage which may be applied continuously between the terminals of the MOV. For all types of AC voltages, the voltage level determination is given by the crest voltage \times 0.707 [1].

Voltage at 1mA or Varistor Voltage (V_{1mA}):

When the DC voltage is adjusted so that a current of Im A is flowing through the MOV, the corresponding value of the voltage is taken as V_{lmA} . At 1ma MOV dissipates a comparatively high amount of power making the MOV hot, which can make the measurement wrong. V_{lmA} is a reference value used in not only in modeling but in comparison of the MOVs.

Maximum Clamping Voltage

This is measured following the standard IEC 60060-2. A current pulse of $8 \text{ u/s}/20 \text{ u/s}$ (rising time/ decreasing time) is sent to the MOV. The selected value ofthe current is the class current. [1]

Maximum Non-Rcpetitive Surge Current

The shape of the pulse, number of pulses and duty cycle of the pulse are the factors that determine the maximum surge current allowed through the device. The maximum non-repetitive surge current is a characterization parameter which demonstrate the capability of the MOV in withstanding surge currents. Beyond this current, MOV will experience catastrophic failure, which needs to be avoided using a series fuse of suitable rating. [1]

1.2 Research Motivation

An incident has been found in a scrap metal melting factory in Sri Lanka, where an induction furnace is operated for melting scrap metal. Frequent failure of surge arrestors in this location is reported. The level of voltage distortion at the installation is about $5 - 10\%$ (at 33kV level measured through a voltage transformer). Therefore, it is suspected that the level of voltage distortion has a relationship with the premature failure of surge arrestors.

1.3 Objectives

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The main objective of this study was to find out the effect of supply voltage harmonics on the lifetime of the MOV type surge arrestors. Sub objectives are:

- 1. Studying the leakage current behavior of MOVs under different levels of supply voltage distortion due to harmonics.
- 2. Proposing methods/remedies for selecting suitable surge arrestors for harmonic rich environments if voltage harmonics cause problems to the MOVs.

1.4 Methodology

A representative batch of MOVs is selected to conduct the experiments. Using a suitable voltage source, distorted waveforms are generated by varying harmonic orders, amplitudes and phase angles. These voltages are fed into the selected MOVs and leakage current is measured using a suitable measurement equipment. Then the MOV is modeled in MATLAB Simulink and the model is tuned to match with the measurement data.

In the next experiment, the same MOV samples are fed with DC voltages and the surface temperature rise is measured against the different power dissipation levels. ANSYS is used to model the MOV thermally and the model is tuned to match with the results of the experiment.

Using electrical model, different levels of distortions are simulated with the MOV model and power dissipation, leakage current values are obtained. Then the power dissipation values are fed to the thermal model to estimate the possible temperature rise ofthe MOV. Using literature, the effect on MOV lifetime is estimated due to the rise of the temperature.

Finally, the maximum allowed distortion levels described in the IEEE-519-2014 standard are taken into consideration to and the possible maximum effect on lifetime is calculated. Based on these Findings, recommendations are mode as remedial actions to the existing selection criteria of maximum continuous operating voltage of surge arrestors.

1.5 Outline of the Thesis

Chapter 2 is the literature study conducted which describes the related work carried out prior. Furthermore, it identifies the research gap between the proposed work and literature.

Chapter 3 describes the thermal and electrical experiments performed on selected MOV samples. Further, it describes the equipment used for measurement and voltage sources.

Chapter 4 is focused on the simulation studies. The first part contains the electrical domain modeling conducted using MATLAB while the latter part is about the thermal domain modeling conducted in ANSYS.

Chapter 5 describes the lifetime estimation model. Using this model together with thermal and electrical models, lifetime estimation is done for various scenarios.

Chapter 6 is a discussion about the overall work earned out under this research.

Chapter 7 presents the conclusions made based on the findings of chapter 5. Further, the recommendations made based on the findings are presented.

2. LITERATURE REVIEW

2.1 Introduction

In this section, the literature on the operating principles of MOVs and their interaction with system voltage is discussed. The focus is kept on the behavior of MOVs under system rated voltage but not due to surges under the scope of this work. Under these conditions, instead of high current short period surges, continuous current condition is experienced for a prolonged period which degrades the MOV eventually.

2.2 Continues Current Condition through MOV

When in operation, a continuous leakage current is conducted through the MOV. Generally, this current is in the range of 10 μ A to 300 μ A in a healthy MOV. In the case of AC, this leakage current is comprised of two components namely resistive component and capacitive component. The higher the resistive current, and the period of exposure, the greater the effect on MOV's life expectancy.

The following studies have described the degradation of MOVs when exposed to dc or 50Hz sinusoidal waveforms of voltage.

1. In their work, Eda, Iga, and Matusoka [3] used 14mm diameter 1.8mm thick ZnO blocks to study the effect of DC and AC voltage bias and temperature bias on the thermal runaway of MOVs. They used a constant current DC supply to measure the V-I characteristic curves. A capacitance bridge is used to measure the capacitance and dielectric properties at ¹ Vrms in the frequency range of 500Hz to 1MHz. With no bias voltage applied, the thermally stimulated current (TSC) measurement is done in a quartz tube by changing the ambient temperature at a rate of 0.333 °K/s. They found that the V -I characteristics of ZnO changed after applying DC and AC bias due to the deformation of the Schottky barrier.

The finding of this works suggests that both ac and dc leakage current can drive the arrestor to its end of life. The operating temperature is found to be a major influencing factor for the expected life of the ZnO.

2. Zhou, Zhang, and Gong [4] worked on the degradation of low voltage ZnO MOVs caused by long time dc biasing based on the V - I characteristic tracing. At an environmental temperature of 140°C, a DC bias of 0.75 V_{lma} is applied to ZnO blocks up to 120 hours. After this treatment, the V-I curve of the ZnO block is traced and compared with the original curve. One of the results obtained is shown in figure 2-1 below.

Figure 2-1: ^V -1 Characteristics of the ZnO block before and after degradation

The results of this study suggest that due to the long time DC bias. The leakage current increases significantly as shown in figure 2-1. Further, they have found that breakdown voltage decreased after the degradation.

3. D. Kladar, F. Martzloff and D. Natasi [5] have discussed Temporary Over Voltage (TOV) effects on surge-protective devices. Though the SPDs can protect downstream systems from surges, their behavior of protection against the TOV (Temporary Over Voltage) is weak. Most of the situations, MOV will be the victim. This work reports TOV susceptibility tests on SPDs.

Authors have applied the following situation equivalent stress levels to MOVs and studied failures.

- a. Poor Voltage regulation
- b. During a fault
- c. Loss of secondary neutral
- d. Ferroresonance
- e. Commingling (Contact to HV circuits)

Typical commercial surge-protective devices are vulnerable to possible TOV conditions in the power systems. SPD's responses to TOV vary from no damage to complete destruction.

Authors has concluded that for SPDs to withstand for the tested TOV conditions, selecting proper MCOV value and fast-acting disconnection device is paramount important. A lower MCOV results in higher TOV originated surge protection device failure rate. When the response time of the disconnection device is longer, the bigger will be the damage. Fast-acting disconnector and SPD with higher MCOV required for maximizing safety.

4. In this paper, Yuanfang Wen and Chengke Zhou [6] presents a methodology for predicting the lifetime of metal-oxide varistor which is widely used as a protective device in electrical and electronic transient overvoltage protection. The methodology followed in this research is based on the characteristic timecurrent curve which is determined using the characteristic currents obtained from several stressed aging experiments. Results have shown that the proposed method is more effective and reliable than previously used methods.

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Aging tests for MOV life prediction categorized into either U method or T method. The so-called U method is to test MOV with the temperature kept constant and voltage being varied to find the characteristic point in I(t) in time tO while the T method is to keep the applied voltage as a constant and to vary the operating temperature. The envelope or critical point (pint at which failure start) of $I(t)$ curve of the U and T methods are plotted by doing a large number of experiments. By curve fitting to the envelope curve, authors have predicted lifetime of MOV under different voltage and temperature conditions.

This method is an experiment intensive method. Therefore, to practically apply to a particular MOV type, it is needed to estimate parameters by using experiments. Therefore, this method is not user-friendly.

- 5. Jaroszewski, Kostyla, and Wieczorek [7] simulated MOV in MATLAB. They tried to study the effect of the presence of harmonics in the supply voltage towards the leakage current based diagnosis techniques used for HV arrestors. Usually, the harmonic presence ofleakage current is used to estimate the level of degradation which is correct for pure sinusoidal voltage supplied. Using the MOV model available in MATLAB, by adding a capacitor in series they performed several numbers of simulations. The findings are listed below.
	- a. The third harmonic **current** is significantly increased when harmonics are present is the supply voltage.
	- b. The appearance of the third harmonic current can be due to the operation of MOV beyond its MCOV.
	- c. The presence of harmonics in the supply voltage can cause misdiagnosis. Therefore, the presence of the harmonics in the supply shall be studied before making conclusions.

This work reveals that the THC can't be utilized for condition monitoring and performance analysis assessment of MOV arrestors operating under distorted supply voltages. This work has no attempt taken to quantify the effect of the presence ofharmonics towards misdiagnosis or a method to do a compensation to the diagnosis when the supply voltage is distorted.

Another important finding is that the high current conduction through varistor devices results from operation voltage higher than rated MCOV. High current is a cause for the thermal run off of MOV.

6. MOV's leakage current is essentially comprised of two components namely resistive and capacitive current components which are orthogonal. Measuring the leakage current is useless for arrestor diagnosis unless the magnitude of the resistive component is extracted. If the supply voltage waveform is known, the resistive component can be extracted easily. Unfortunately, there for MV and HV arrestor diagnosis, there is no way to measure the voltage waveform. As a solution to this, Abdul-Malek, Yusof and Yousof [7] proposed the modified shifted current method (MSCM) algorithm, to extract the resistive current component from the measured total leakage current of MOV arresters.

By adding a quarter cycle shifted leakage current to the original leakage current waveform, the resistive component can be subtracted from the waveform and thereby the capacitive component can be extracted. By experimenting with a real arrestor, the proposed scheme has been verified.

This technique is useful in situations where voltage waveform cannot be measured (HV installations). In such situations, by analyzing the leakage current waveform, it is possible to decompose the resistive and capacitive current components. The resistive current component is the indicator of the arrestor condition. However, by deeply studying the algorithm of MSCM, it is found that this technique is valid only when the supply voltage waveform is sinusoidal. Therefore, this method is not applicable for condition monitoring of arrestors attached to power systems having distorted voltage sources.

2.3 Conclusions

Failure of MOSA/MOV can be due to two reasons. The first and most popular is the conduction of high surge currents through the device. These currents are in the range of few to few tens of kilo Amperes. If the current is above the maximum permitted surge current, MOV will fail in the first occurrence of the surge. When the current is less than the maximum permitted surge current, MOV will perform its duty by clamping the surge voltage to an acceptable value. MOV degrades at each surge current conduction. Leakage current through the MOV at power frequency voltage will be increased thereafter and the clamping voltage of the MOV will be increased. This will result in a decreased level of guaranteed protection to the downstream systems and eventual thermal runaway of MOV.

The second way is the continuous current condition through MOV at power frequency voltage due to long-term continuous exposure to ac or dc voltages. Shift of the $V \sim I$ characteristics, increased leakage current, etc. are the symptoms of failures. The phenomena found in the literature which can cause MOVs to fail due to long term conduction are listed below.

- f. Poor Voltage regulation
- g. During a fault
- h. Loss of secondary neutral
- i. Ferroresonance
- j. Commingling (Contact to HV circuits)

Studies have been conducted to evaluate the effects of the above phenomena and found that the response varies from damage to destruction. The response can be diverse due to different brands, MCOV, and clamping voltage levels.

At present, the magnitude and spectral composition of leakage **current** is used as an indicator for the diagnosis of surge arrestors. The validity of these techniques in environments with distorted supply voltages is questionable. Therefore, a need is found in the knowledge, on a technique to access the life expectancy of MOVs working in harmonic rich environments for prolonged periods. Therefore, a research avenue is found to study the effect of harmonic related overvoltage on the life expectancy of MOVs.

3. EXPERIMENTAL WORK

3.1 Introduction

This chapter describes the experiments conducted to measure leakage currents of selected MOV samples under distorted and pure supply voltage conditions. The selection of voltage sources, measurement equipment, and environmental conditions are described as well.

3.2 Selection of Equipment

3.2.1 AC Voltage Source

The purpose of this study demands to measure the leakage currents of MOV samples under distorted supply voltages. For this purpose, it is required to generate waveforms with a controlled amount of distortion by superimposing a known number of harmonics with different levels of magnitude and phase angles.

The first approach was to use a function generator available. The main problem found is, function generator was its peak to peak amplitude limitation in addition to the limitation of the control of waveform shape. The solution found is the voltage source available inside the meter testing bench used for testing energy meters. This source allows the generation of waveforms with a known amount of distortion at a fixed RMS value. Details of the source is given below.

Make: ASTeL Model: VIS-400 Output Voltage Range:30Vrms -350V RMS Harmonics Generation: Programmable Output Frequency Range: 50 Hz to 2500Hz

3.2.2 DC **Voltage** Source

A DC voltage source is used to take the temperature measurements of MOVs concerning the leakage current through the MOV. The relationship between the internal power dissipation and surface temperature rise is developed using these measurements. Details of the source is given below.

Make: Ametek California Instruments Model: 1501IX Output DC Voltage Range: $0 - 400V$ Resolution: IV Frequency Range: 16 - 1000Hz Accuracy: 0.025% Voltage Accuracy: 0.1% FS Current Accuracy: 0.5% FS

3.2.3 Measurement Equipment

Yokogawa WT310 power analyzer is used as the measurement equipment. It can measure the RMS value of the leakage current upto of micro Amperes. Additionally, through the computer interface, the voltage and the current waveforms can be visualized. Up to 50th, harmonics can be measured in both voltage and current waveforms.

	WT310.WT330	WT310HC	WT310HC
	(Voltage/Current)	(Voltage, Current EXT) sensor input)	(Current Direct input)
DC.	\pm (0.1% of reading	\pm (0.1% of reading	\pm (0.2% of reading
	$+0.2\%$ of range)	$+0.2\%$ of range)	$+0.2\%$ of range)
0.5 Hz \leq f $<$ 45Hz	\pm (0.1% of reading	\pm (0.1% of reading	\pm (0.1% of reading
	$+0.2\%$ of range)	$+0.2%$ of range)	$+0.2\%$ of range)
$45Hz \le f \le 66Hz$	\pm (0.1% of reading	\pm (0.1% of reading	\pm (0.1% of reading
	$+0.1$ % of range)	$+0.1$ % of range)	$+0.1$ % of range)
$66Hz < f \leq 1kHz$	\pm (0.1% of reading	\pm (0.1% of reading	\pm (0.1% of reading
	$+0.2$ % of range)	$+0.2$ % of range)	$+0.2$ % of range)
1kHz $<$ f \leq 10kHz	\pm {(0.07 × f)% of reading	\pm {(0.07×1)% of reading	$\pm ((0.13^{\circ} \text{f})\% \text{ of reading})$
	$+0.3$ % of range)	$+0.3$ % of range)	$+0.3$ % of range)
$10kHz < t \leq 20kHz$			\pm ((0.13*f)% of reading $+0.5%$ of range)
10 kHz < $f \le 100$ kHz	\pm (0.5 % of reading $+0.5$ % of range) \pm {{0.04 \mathbb{(f-10)}% of reading	\pm (0.5 % of reading $+0.5$ % of range) \pm {{0.04×(f-10)}% of reading	

; **Figure 3-1: Accuracy data. Yokogawa WT310 Power Meter**

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Using the table above, the maximum possible error is calculated for the measurement range current 0 to 5mA and voltage 0 to 300V. The results are given in the figure 3-2 below (details available in Appendix IV).

Figure 3-2: Measured Value (mA) vs $Error (\mu A)$

The typical leakage current of MOV is in the range of 30 to 300 μ A. During the experiments, it is expected to measure currents in the range of 100 to 1000 μ A. The maximum possible error within this range is $12 \mu A$. Therefore, this equipment is considered suitable for taking measurements.

To take temperature measurements, a digital thermos meter with k type thermos couple is used. Details are given below.

Make: Omega

s

Model: HH506RA

Minimum Temperature Measurement: Type K -200°C (-328°F) J -210°C (-346°F) T - 200°C (-328°F) E -210°C (-346°F) R 0°C (32°F) S 0°C (32°F) N -50°C (-58°F) Maximum Temperature Measurement: Type K 1372°C (2501°F) J 1200°C (2192°F) T 400°C (752°F) E 1000°C (1832°F) R 1767°C (3212°F) S 1767°C (3212°F) N 1300°C (2372°F)

TH 4268

Resolution: 0.1 °C.

*

Accuracy: K/J/T/E Type: $\pm (0.05\% \text{ reading} + 0.3\degree \text{C})$ on -50 to 1370°C

3.3 MOV Samples

Commonly available Disk type MOVs have been selected from the local market. Samples with the same ratings selected from the same brand and the ratings are given below table 3-1.

Maximum Allowed Voltage (AC)	230V
Voltage at 1mA leakage current (DC)	360V
Maximum Clamping Voltage	595V at 50A
Withstanding Surge Current	4500A
Rated Power	0.6W
Typical Capacitance	560 pf

Table 3-1: Ratings of the Selected MOV Samples

3.4 Leakage Current and Power Dissipation Measurement When Supply Voltage is Distorted

This experiment is performed on three samples from the above-selected MOVs. MOV was mounted on an energy meter enclosure so that it could be easily mounted on the meter testing bench. Yokogawa power analyzer was connected appropriately for measuring the voltage across MOV and the current flowing through the MOV. The software package WT Viewer is used to acquire the data to a PC.

The circuit arrangement of the experimental setup is shown below.

Figure 3-3: Circuit Diagram of MOV Testing Experiment

The photograph of the experimental setup is shown below as figure 3-4. The power meter and MOV under test are visible. The voltage source is not visible since it is an integral part of the meter testing bench,

Figure 3-4: Experimental Setup

Using the computer interface of the voltage source control software, different waveforms created keeping the RMS value of the voltage at 230V. The experiment started with a pure sinusoidal voltage output. Harmonics were added gradually for a certain number of situations (assumed to be occurring in power systems). The waveform of one of the tested cases is shown below figure 3-5.

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Figure 3-5: Leakage Current Waveform for Distorted **Supply Voltage of 230Vrms** (Fundamental + $5th$ Harmonic 20%)

Details of all experiments performed are shown in Appendix 1.

3.5 Power Dissipation vs **Temperature Rise Measurement**

Under this experiment, a DC source is utilized to supply MOV with a DC voltage. Calculated power dissipation and temperature rise measurements are noted down. Experiment was conducted inside a laboratory having a controlled temperature of 25°C. The voltage across the MOV is varied from 0 to its V_{lmA} (V). The collected measurements of the experiment is shown below table 3-2.

Table 3-2: Heat Dissipation and Temperature Rise Measurements of MOV

Samples

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At VimA, MOV exhibited a surface temperature rise close to 10°C and proportional temperature rise for power dissipation values in between. These experiments give clear evidence of the linear relationship between the power dissipation and the temperature rise of the MOV. The relationship of temperature rise against the current and voltage are non-linear due to the nonlinear relationship between voltage and current behavior. Therefore, it is convenient to use the relationship of temperature rise against the power dissipation for the scope of this study. The above data is shown on the chart below figure 3-6 which explains the linear relationship.

Figure 3-6: Temperature vs Power Dissipation

CHAPTER 4

4. SIMULATION STUDIES

4.1 Introduction

This chapter describes the corresponding simulations done for the selected model of MOV samples. MATLAB Simulink software is used for the electrical domain simulation of MOVs. The thermal modeling of MOVs is done using the ANSYS software. The models used are tuned and modified to match with the MOV samples. Using simulation models, different scenarios of voltage distortion studied.

4.2 MOV Modelling **in** Electrical Domain

4.2.1 MOV Model in MATLAB Simulink

There is a block available for MOV in MATLAB Simulink platform [19]. Details of the available model is given below figure 4-1.

Figure 4-1: MOV Model in MATLAB **Simulink.**

The MOV model in Matlab implements a nonlinear resistor having similar characteristics to MOVs discs used for protecting power equipment from transient overvoltages. Multiple of MOV blocks are cascaded for high voltage applications while series combinations used to dissipate more power form transients. The nonlinear V-I characteristic of each MOV column of the protection device is modeled by a combination of three exponential functions of the following form [19].

$$
\frac{V}{V_{ref}} = k_i \left(\frac{I}{I_{ref}}\right)^{\frac{1}{\alpha_i}}
$$

$$
i = 1, 2, 3
$$

 $V = Voltage$

Vref = Reference Voltage

 $I =$ Current

;

Iref= Reference Current

 k_i = Proportionality Constant of segment i

 $1/\alpha_i$ = Tangent of angle curve deviating from the horizontal of segment i

4.2.2 Mode! Parameters [19]

Table 4-1: MOV Modelling Parameters

i

To simulate a single MOV, the following parameter values used

Table 4-2: Parameters Used During Simulation

4.2.3 Modified MOV Model

i

From the literature, it is found that a disc type MOV can be modeled by the following equivalent circuit [1].

Figure 4-2: MOV Equivalent Model

At low currents, the model can be simplified to the following equivalent circuit [1].

Figure 4-3: MOV Equivalent Circuit at Low Currents

The MOV model available in MATLAB has neither a capacitive component nor an inductive component. Therefore, to make the MATLAB model more realistic, the parallel capacitance with the Resistance is added. The value of the capacitance is obtained by measurement. The average value of capacitance for the selected samples is 560µF. The value of lead inductance is considered negligible.

The final version of the model used in the simulation is shown below.

Figure 4-4: MATLAB Simulation Model of MOV

4.2.4 Simulation and Results

The modified MOV model is simulated by feeding it by a distorted voltage source. Current and voltage measurements are taken in addition to the plotting of waveforms. Due to the reason, of distorted current and voltage waveforms, the RMS value is taken as the measurement. Both resistive and capacitive current components of the leakage current is measured during the simulation since it is readily available. The block diagram is shown below as figure 4-5.

When adding harmonics on top of the fundamental to distort the waveform, the magnitude and phase angle has an infinite number of combinations. The magnitude value is limited to the practical values depending on the order of the harmonic. The phase angle is selected in such a way that the peak value of the final waveform is a maximum. Selected magnitude and phase angle pairs for different harmonics simulated are shown in the following table.

■r

a

Harmonic Order	Maximum Magnitude as a percentage of fundamental	Phase Angle
	30%	180
	20%	
	10%	180
	5%	
		8ſ

Table 4-3: Harmonic Percentage Values and Phase Angles

The base case of the simulation is the MOV feeding with a sinusoidal voltage waveform. The results of the base case are given in figure 4-6 below.

Figure 4-6: Applied Voltage (Ub), Resistive Current Component through MOV (lb: MOV nonlinear Resistance), Capacitive Current Component through MOV (lb: MOV Capacitance), and Total Current through MOV (lb: Series RLC Branch)

The RMS values of the simulated voltage and currents are as below table 4-4.

Table 4-4: Simulation Measurements When Pure Sinusoidal Waveform Applied to MOV

An example measurement made with 230V RMS voltage waveform distorted with 20% of 5th harmonic is shown in figure 4-7 below.

Figure 4-7: Applied Voltage (Ub), Resistive Current Component through MOV (lb: MOV nonlinear Resistance), Capacitive Current Component through MOV

(lb: MOV Capacitance), and Total Current through MOV (lb: Series RLC Branch)

The RMS values of the simulated voltage and currents are given below table 4-5.

Table 4-5: Simulation Measurements When 5th Harmonic 20% Distorted

Several number of simulation runs were done. Results are available in Appendix II.

4.2.5 Discussion

1

In this simulation study, MOV is represented using a parallel amalgamation of a nonlinear resistor and a capacitor. Temperature dependence ofthe nonlinear resistor is not taken into consideration assuming it is not significant. As per the observations made, the behavior of resistive current component is dependent on the instantaneous voltage of the waveform. Higher the peak of the waveform, the higher the resistive current is. Even though the RMS value of the distorted and non-distorted voltage waveforms are the same, the resistive leakage current has a significant difference due to the peak value difference. The resistive heating effect inside the MOV core is due to this resistive current component. Therefore, the heating effect of MOV can be higher when the voltage is distorted due to harmonics.

The capacitive current component is comparatively lesser than the resistive current component. Therefore, its contribution to the total leakage current through the device is less. With the frequency, an increase in the capacitive current is observed and it is since capacitive reactance is inversely relative to the frequency. The variation of resistive and capacitive currents for different cases of simulation is shown in the following figure.

I

Figure 4-8: Leakage currents for different levels of distortion (I: Total leakage, Ir: Resistive component and Ic: Capacitive Component)

When examining the above charts, it is clear that in almost all the cases, the leakage current is high in the distorted scenarios though the RMS value of supply voltage waveform is the same 230V. Higher resistive leakage current will result in premature failure for the MOV.

If the case of distortion with $3rd$ harmonic 30%, the rise of resistive current compared to non-distorted case is approximately 20 times. That is analogous to the rise in power dissipation of 20 times. At no circumstance, this level of leakage current is acceptable. In practice, persistent resistive leakage current more than 1mA is not acceptable. In the long term, persistent leakage current of more than 0.5mA is harmful to the MOV.

4.3 MOV Modelling in Thermal Domain (Steady State)

4.3.1 ANSYS Simulation

Using ANSYS software, a simple **thermal** model of MOV is developed to see **the thermal** behavior of MOV under persistent internal heat generation due to leakage current. Body part of a 20mm MOV is modeled and its core is set as a heat source where uniform heal generation **occurred. In** Ansys, heat generation is accepted as Watts per unit volume. The heat generated from the experimental and simulation work carried above was converted to per unit volume values and applied to the simulation. Details of the simulation is shown below figure $4-8$ [1].

Figure 4-9: Cross Section of the Modelled MOV

Figure 4-10: Temperature Distribution of MOV Model in ANSYS

Ambient temperature was set to 25° C. The cooling of the MOV is due to the convection and radiation mainly. The effect of conduction through the leads is assumed to be negligible. The film coefficient and surface emissivity are adjusted to tune the model with measurement data. Film Coefficient: $25W/m^2$ °C and Emissivity 0.95 resulted in the best fit of measured data with simulations.

per Unit Volume

4.3.2 Model Validation

Table 4-6: Simulated and measured temperature data

The error between measured and modeled temperature rise is assumed to be normally distributed. Calculated mean is -0.1 °C and a standard deviation of0.2 °C. Accordingly, the 95% confidence interval is -0.5 °C to 0.3 °C. Therefore, the model is assumed to be fairly accurate for this study.

4.3.3 Results and Discussion

The purpose of this thermal simulation is to build a relationship with the internal heat generation and surface temperature rise. It is the surface temperature rise which is related to the premature failure of MOV. Results of the regression done for the simulation results for the relationship between internal heat generation and surface temperature rise is shown below.

SUMMARY OUTPUT

From the regression, the following model is derived for simplification of calculation of temperature rise related to a particular heat dissipation inside MOV.

Surface Temperaure Rise

$= 25.01 + 1.37 \times 10^{-5} \times$ *Heat Generation per Unit Volume*

From the regression analysis, it is clear that the surface temperature rise has a direct linear relationship with the internal heat generation per unit volume. This equation will be used to calculate the temperature rise of MOV, by using measured or simulated power dissipation of MOV.

5. ESTIMATION OF EFFECT OF SUPPLY VOLTAGE distortion ON LIFETIME OF MOV

5.1 Introduction

In this chapter, the experimental and simulation work carried out is utilized to estimate the effect of supply voltage distortion on the lifetime expectancy of MOVs. Using the results of the experiments, the thermal and electrical models are verified. Using the electrical mode, the resistive leakage current and power dissipation of the MOV is obtained.

This power dissipation value is fed into the thermal model to calculate the temperature rise. The temperature rise value is used to evaluate the life expectancy using, life expectancy models available in the literature.

5.2 Lifetime Estimation Model

In literature, no life expectancy estimation models are found for MOVs relating to their body temperature. The well-known Arrhenius equation has widely been used for accelerated lifetime testing. The standard IEEE C62.11-2012 [9], IEEE Standard for Metal-Oxide Surge Arresters for AC Power Circuits (>1 kV) has the following information on accelerated life testing.

'Basis for accelerated aging procedure

Arrhenius life tests of metal-oxide block designs that Based on loss with time, thefollowing statements apply: increase in power

temperature for all 40 °C is a conservative weighted average use except liquid-immersed and deadfront arresters. arresters

is accelerated by elevated temperature. The block aging process

The temperature acceleration rate is reasonably estimated by an acceleration factor A_{FT}.

Where

 $A_{FT} = 2.5(11)/(10)$

Where

A ⁷ *is the clifJet ence between test temperature and weighted average use temperature* "

It is understood that the above information in the standard refers to the arrestor operation in an elevated temperature environment. This research discusses about the arrestor operation in nominal ambient temperature with an elevated body temperature. It is assumed that the effect on the life expectancy of MOV is same for the both of the above cases namely operating in elevated ambient temperature and operating in nominal ambient temperature with a higher body temperature.

Hence, I/A_{FT} is equal to the expected life span ratio of the MOV as per the following equation

Life at Elevated Temperature = *Life at Nominal Temperature/Aft*

The I/A_{FT} calculation for different distortion scenarios is shown in the results below table 5-1.

5.3 Results

Table 5-1: Results of Lifetime Estimation for Different Levels **of Distortion**

5.4 **Discussion**

As per the results, harmonic distortion is a reason for the reduction of the lifetime of MOV. In some cases, the life expectancy is as low as 32% while some causes not impose significant reductions. The expected lifetime together with the peak value of the applied voltage wavefonn is shown below figure 5-1 to identify the reason for life **expectation** reduction.

different levels of distortion

As per the above chart, it is cl

ear that the life waveform is higher than the peak of the expectation reduces when the peak of the lesser is the anticipated life span is D at a non-distorted case. The higher the peak is the e waveforms, it is evident that span is. But for the last thre even provided using the following chart. though the peak is the same. The explanation is life expectancy is different

Figure 5-2: Behavior of Peak Value of the waveform for different level of distortions for the same RMS value

The chart above contains fundamental and three different distorted waveforms with the given harmonics in the legend. Their RMS value is the same and the peak is the same. The line V_{lmA} shows the MOVs voltage at 1mA current. Whenever the voltage rises above this line, the current flowing through MOV increases drastically for keeping the Voltage from rising. Whenever the area between input waveform and the line is higher compared to the waveform with the same peak, the current flowing V1mA is also high.

 α is flowing through MOV, there will be more dissipation of power. for the body temperature of MOV to rise. is the reason for premature failure. When more curre That excess power dissipation is a reason Operation at elevated body temperature

IEEE 519-2014 [2] is the standard whtch laid limits to the voltage total harmonic distortion for utility supply voltages. The following table shows the applicable limits.

Table 1—Voltage distortion limits

"High-voltage systems can have up to 2.0% THD where the cause is an HVDC terminal whose effects will have attenuated at points in the network where future users may be connected.

As per the standard, at the point of common coupling (PCC) the above maximum THD values need to be maintained. For LV systems, it is 8.0%. One of the possible worstcase scenarios in the LV system with distortion of 3rd, 5th and 7th harmonics considered. To have a THD of 8% following individual components used.

Table 5-3: Composition of Harmonics for 8% THD

Harmonic	Percentage	Angle
3rd	5%	180
	5%	∩
5th	3.7%	180
7th	8%	
THD		

(

The results of the simulation are as below figure 5-3.

Figure 5-3: Results for 8% THD in Voltage

Resistive Leakage Current: 0.52 mA Power Dissipation: 0.07W Temperature Rise: 2 Degrees Celsius Expected Life: 76% from Rated Life

The implied result is that even under the accepted distortion limit, the selected MOV type is going to suffer a 24% reduction in life in the assumed case. Therefore, selecting critical decision that needs to be taken considering the effect the MCOV of MOSA is a of harmonics.

If $\frac{1}{2}$ is interested dissipation in the devices can increase the total power loss in the system significantly. Depending on the MCOV of the device and peak value of the supply significantly. Depending $\frac{1}{2}$ is going to be increased. This value needs to be calculated for each device separately because of the nonlinear behavior of MOVs. use of the nonlinear behavior of MOVs.

If the tested types of MOVs are available in a transformer area of 250 homes, 5 in each home inside different appliances. Let's consider a worst-case distortion level under IEEE-519-2014 limits.

Under non distorted conditions Power dissipation = $0.018W * 250 * 5 = 22.5W$ Energy loss per day $= 0.54$ kWh

Under distorted conditions Power dissipation = $0.070W * 250 * 5 = 87.5W$ Energy loss per day = 2.1 kWh

Percentage rise in power dissipation in the area = $87.5/22.5 * 100 = 388\%$ By selecting the MCOV value carefully, this situation can be avoided.

CHAPTER 6

6. DISCUSSION 6.1 Introduction

In this chapter, a discussion is followed by a comparison with the available literature. Further, the validity of the contract of the contract of the value of the value of the value of the contract of the contract of the contract of the contract of the co results obtained are discussed and the feasibility of applying them for MV surge arrestors made from MOV discs such is presented about the findings of this research work

6.2 Results **in** contrast **with Literature**

Both of the simulation studies and experiments performed revealed that distorted voltage waveforms applied to MOV surge arrestors can increase the steady-state p ower dissipation. The reason behind this possibility is identified as the peak voltage rise of the voltage waveform due to the harmonics. Some combinations of harmonic order and phase angles increase the peak of the waveform when added on the fundamental.

Due to the increased power dissipation, MOVs will stay hotter throughout their operational life which will be a reason for premature failure. By nature, MOVs are capable of dissipating energy of transients which are very short in duration. A tradeoff of increased clamping voltage is experienced every time the MOV dissipated energy of a surge. This behavior makes the device useless after a certain number of strokes since it loses the capability of providing the required level of protection to the downstream.

odel validation forsimulation in electrical and thermal domains. The adverse effect of operation at elevated body temperature was downstream.
Experiments performed followed with model validation for simulatic
diverse effect of operation at elevated body

Eda, Iga, and Matusoka [3] have concluded from their work that, the amount of leakage μ d Matusoka [3] have expectancy of MOVs. current, as

Zhou, Zhang, and Gong [4] studied the

varistors caused by dc biasing at elevated to phenomena of low voltage ZnO temperature and DC bias V-I characteristic as voltage. The work presented ⁱ the meantime, gone further by quantifying the eristics of MOVs degrad In this thesis has strengthened the above findings and, in e increasing the clamping the meantime, gone further by quantifying the adverse effect on lifetime.

D. Kladar, F. Martzloff and D. Natasi [5] have discussed TOV effects on surgeprotective devices. Since the TOV on SPD implies the increased leakage current, the expected results are as same as the above literatu re and findings of this research.

Yuanfang Wen and Chengke Zhou [6] present a methodology for predicting the lifetime of metal-oxide varistor (MOV), which is a cumbersome method involve a large number of experiments. Therefore, the practical use of the method is highly limited. The method used in this research is simple and in line with the IEEE standard. Therefore, it is envisaged that this work can be more useful in the phase of design.

6.3 Limitations and Applicability for Utility-Scale MV Applications

The work carried out under this research was conducted for LV MOVs. The results derived are applicable mostly for the LV rated MOVs used in electronic devices. When it comes to utility applications, LV MOSAs are rarely used. Medium Voltage systems always are protected using MV rated MOSAs usually at primary substations, distribution substations and other related switchgear like load break switches.

These MOSAs are mode using a stack of MOV discs encapsulated in a suitable housing
Also kehavior deviated from structure [20]. The resultant device has thermal and electrical behavior deviated from structure $[20]$. The resultant device has not resistive leakage current of the MOSA is deviated from the c expected not to have heavily deviated not $\frac{d}{dx}$ heavily changed due to the additional the MOVs used for LV applications. The resident capacitive component of the curre capacitive effects of the construction.

d farther refinement for directly applying Therefore, the derivations of this work nee for MV rated MOSAs.

CHAPTER 7

S AND RECOMMENDATIONS ^{7.} CONCLUSION

This study reveals that the presence of harmonics failure of MOV and hence the MOSAs When he come a reason for premature magnitude and phase angle combinatio armonics are present with specific The peak value of the waveform of the waveform higher than
The peak value of the wave In MUSAs. When harmonics are present with
pmbinations can be a reason to increase the peak
the peak voltage of the non-distorted voltage we
efform has a direct relationship with Resistive current through the MOV. The higher the peak voltage, the higher will be the resistive leakage current and the power dissipation.

Therefore, when selecting MCOV of the MOSA, attention should be paid about the possible maximum system peak voltage. It is better to consider the effect of harmonics on the maximum voltage on top of the other factors. It is not the RMS value of the voltage, but the factor governing the magnitude of the leakage current is the peak value of the waveform.

Another important factor for increased leakage current is the shape of the waveform. When the waveform is wider near the peak, RMS leakage current increases. Operating a MOV at continuous excess power dissipation can shorten the lifetime of IEEE C_6 (2, 11, 2012 [9] standard-based calculations, the loss of the MOV. As per the life expectancy can become significant in some situations

7.1 Recommendations Formining this procession. To minimize this problem following recom

As shown in row number 1, for LV systems, it is recommended to use MCOV values above 390V for Line to Neutral applications. The basis for the selected value is the RMS value of line to neutral voltage multiplied by 1.5 factor from C62.11-2012 [9] standard and by 1.13 factor from IEEE-519-2014 as shown in the table.

Likewise, for $1kV < V \le 69kV$ category CEB has $11kV$ delta and 33V delta systems. It is recommended to use MCOV of llkV in llkV systems and 31.3kV in 33kV systems when selecting MOSAs. It is noted that the values used by CEB is not sufficient in the scenarios where harmonic overvoltages.

For 69k V $\lt V \lt = 161$ kV range, CEB's applications are on 132kV. The suggested value of MCOV in IEEE C62.11-2012 [9] is 115kV. It is proposed to increase this value by 1.04 to match with the possible peak voltage due to distortion. The resultant value is 119.6kV.

7.2 Study Limitations and Suggestions f0 r Future Work

MOVs are under LV conditions tested in voltage harmonics on MOSAs due to the reason for the unavailability of MV/HV test this study to assess the effect of supply equipment and MV/HV harmonic sources. Thus, an avenue is available to extend the experiments to MV/HV levels with real MOSAs but not MOVs.

Further, the behavior of leakage current against the time when exposed to distorted voltages is not considered in this work. The failure point of a MOV can be precisely detected by the magnitude of resistive leakage current when passing a certain limit. Therefore, if the leakage current can be logged continuously to fit a model against time, then it will reveal whether the failure occurs before the catastrophic failure or Arrhenius life estimation.

8. REFERENCES

[1] Littelfuse.com. (2018). [online] http://www.littelfuse.com/~/media/electronics/product_catalogs/littelfuse_varistor_canter and all the 20191

 $[2]$ IEEE 519-2014 - IEEE Recommended Practice and Republican Repu Control in Electric Power Systems. nic

[3] K. Eda, A. Iga, and M. Matsuoka, Degredation ceramics. *Journal of Applied Physics,* 1(5):2678, 2684 anism of non-ohmic zinc oxide

[4] D.Zhou, C.Zhang, and S.Gong. Degradatio voltage ZnO varistors. *Materials Science and Engineeries* due to dc bias in low 2003. *nee and Engineering: ^B*, ¹ -3(99):412-415, May

[5] D. Kladar, F. Martzloff and D. Natasi. TOV Effects on Surge-Protective Devices.
Power Quality Exhibition and Conference Balting Power Quality Exhibition and Conference, Baltimore, October 25-27 2005.

[6] Y. Wen and C. Zhou. A Novel Method for Predicting the Lifetime ofMOV *.IEEE transactions on power delivery*, vol. 19, no. 4, October 2004.

[7] M.Jaroszewski, P. Kostyla, and K. Wieczorek. Effect of voltage harmonics content on arrester diagnostic result. *Proceedings of the International Conference on Solid Dielectrics (ICSD 2004), Toulouse, France*, July 2004.

[8] Technical note on Capacitor lifetime. Rubicon Corporation. [[www.rubycon.co.jp/en/products/alumi/pdf/Life.pdf\]](http://www.rubycon.co.jp/en/products/alumi/pdf/Life.pdf).

[9] IEEE Std C62.11-2012 IEEE Standard for Metal-Oxide Surge Arresters for AC Power Circuits (>1 kV).

[10] M. Balci and M. Hocaoglu. On the validity of harmonic source detection methods *14th International Conference on Harmonics and* [10] M. Balci and M. Hocaoglu. and indices. *Proceedings of the* **Quality** of Power, Bergamo, Italy, September 2010.

[11] R. Herrera, P. Salmeron, and S. Litran. Assessment of harmonic distortion sources in power networks with capacitor banks. *Proceedings of the 11th International Conference on Renewable Energies and Power Quality, Las Palmas de Gran Canary, Spain,* April 2011.

[12] C.de Salles, M. L. Martinez, and due to surges. *Proceedings of the International Symposium on Lightning Protection (XI SIPDA), Fortaleza, Brazil* October 2011. *iroz. Ageing ofmetal oxide variators LightningProtection on*

[13] R.A. Sargent, G. L. D unlon and λ .

currents on the microstructure and $_{\text{elect}}$ $_{\text{C}}$. Darvenizal Effectrical oxide variations. *transactions on Electrical Insulation 6(3) secured* properties of metal

Aging of the over-voltage 1959 -1966, December 2002.

₁₉₅₉ -1966, December 2002. [14] P.Osmokrovic, B. Loncar, S. Stankovic protection elements caused by over-voltage and S.Vasic.

[15] C.Nahm. Pulse aging behavior of Z_{no} with sintering time. Ceramics international $37(6)$, 140° (203 -dy2o3 varistor ceramics with sintering time. Ceramics international, 37(6):1409{1414, May 2011.

[16] W. Sun, Y. Li, D. Zeng, R. Cai, and Z. Meng G. Yang. Investigation of voltage current characteristics of ZnO varistors under 8/20 microsecond impulse current. The *IEEE Power Engineering and Automation Conference, Wuhan, China, September*

[17] C. Karawita and M.Raghuveer. Leakage current based assessment of degradation of MOSA using an alternative technique. *Annual Report of the Conference on Electrical Insulation and Dielectric Phenomena (CEIDP'05),* pages 199-201, October 2005

[18] J. He, R. Zeng, Y. Tu, S. Han, and H. Cho. Aging characteristics and mechanisms of ZnO nonlinear varistors. *Proceedings of the 6th International Conference on Properties and Applications of Dielectric Materials, Xi'an, China, June 2000.*

[19] MATLAB Simulink Documentation. (2019). [online] Available at: https://www.mathworks.com/help/simulink/ [Accessed 4 Jun. 2019].

[20] Medium-voltage surge arresters production video (smaller format). (2019).
https://search[video] Available at: <https://search-> μ _{t.}com/library/Download.aspx?DocumentID=9AKK107046A9495&Language Code=en&DocumentPartId=Lower%20resolution&Action=Launch [Accessed 19 Nov. 2019],

[21] V.P. Rabde. Metal oxide varistors as surge suppressors. Proceedings of the 1995 *International Conference on Electromagnetic Interference and Compatibility (INCEMIC),* Madras, India.

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Appendix II Results of the Simulations Performed

CASE 2 - 3RD HARMONIC DISTORTION

Fundamental * 30 % 3'd Harmonic Harmonic Phase Angle: 180 degrees

Resistive Leakage Current: 2.44mA rms Power Dissipation: 0.39W Temperature Rise: 10.8°C Expected Life : 37% from rated life

CASE $2 - 3^{RD}$ HARMONIC DISTORTION

Fundamental \pm 30 % 3rd Harmonic **(A)** $\frac{w_0}{w_0}$ Harmonic Phase Angle: 180 degrees

•J*SL*--------- ^t **(l** ^I Resistive Leakage Current: 2.44mA rms Power Dissipation: 0.39W Temperature Rise: 10.8°C Expected Life: 37% from rated life

CASE 2-5th HARMONIC DISTORTION

Fundamental ⁺ 20 % 5th Harmonic Harmonic Phase Angle: o degrees

Resistive Leakage Current: 0.41mA rms Power Dissipation: 0.10W Temperature Rise: 2,8°C Expected Life: 78% from rated life

CASE ³ _ 7™ HARMONIC DISTORTION

Fundamental + 10 % 7th Harmonic Harmonic Phase Angle: 180 degrees

Resistive Leakage Current: 0.17mA rms Power Dissipation: 0.04W Temperature Rise: 1.1°C Expected Life 90% from rated life

$CASE 4 - 9th HARMONIC DISTORTION$

Fundamental + $5%$ 9th Harmonic Harmonic Phase Angle: 0 degrees

Resistive Leakage Current: 0.12mA Power Dissipation: 0.02W Temperature Rise: 0.6°C Expected Lite : 95% from rated life

FUNDAMENTAL $+ 3^{RD}$ HARMONIC $+ 7^{TH}$ HARMONIC

Fundamental

- ⁺ 3rd 20% (180 degrees)
- ⁺ 7th- 10% (180 degrees)

Power Dissipation: 0.39W Temperature Rise: 11° C Expected Life : 38% from rated life

Appendix III

Measurements of MOV \rm_{Volta} ge at 1mA DC C urrent

Appendix IV

Yokogawa WT310 M easurement Error Analysis

